

E:/WORK/STM32/STM32
_ExtendedLibs/MyLibs
/Inc/bench_time.h

```
graph TD; A["E:/WORK/STM32/STM32_ExtendedLibs/MyLibs/Inc/bench_time.h"] --> B["mylibs_defs.h"]; A --> C["stdint.h"]; B --> D["mylibs_config.h"]; D --> E["stm32f4xx_hal.h"];
```

mylibs_defs.h

stdint.h

mylibs_config.h

stm32f4xx_hal.h