

F:/Work/Projects/STM
/.Elementary/STM32_Extended
Libs/MyLibsGeneral/Inc/mylibs_defs.h

```
graph TD; A["F:/Work/Projects/STM  
/.Elementary/STM32_Extended  
Libs/MyLibsGeneral/Inc/mylibs_defs.h"] --> B["mylibs_config.h"]; B --> C["stm32f4xx_hal.h"];
```

mylibs_config.h

stm32f4xx_hal.h